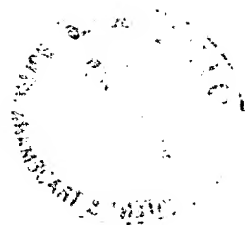


FIG. 1



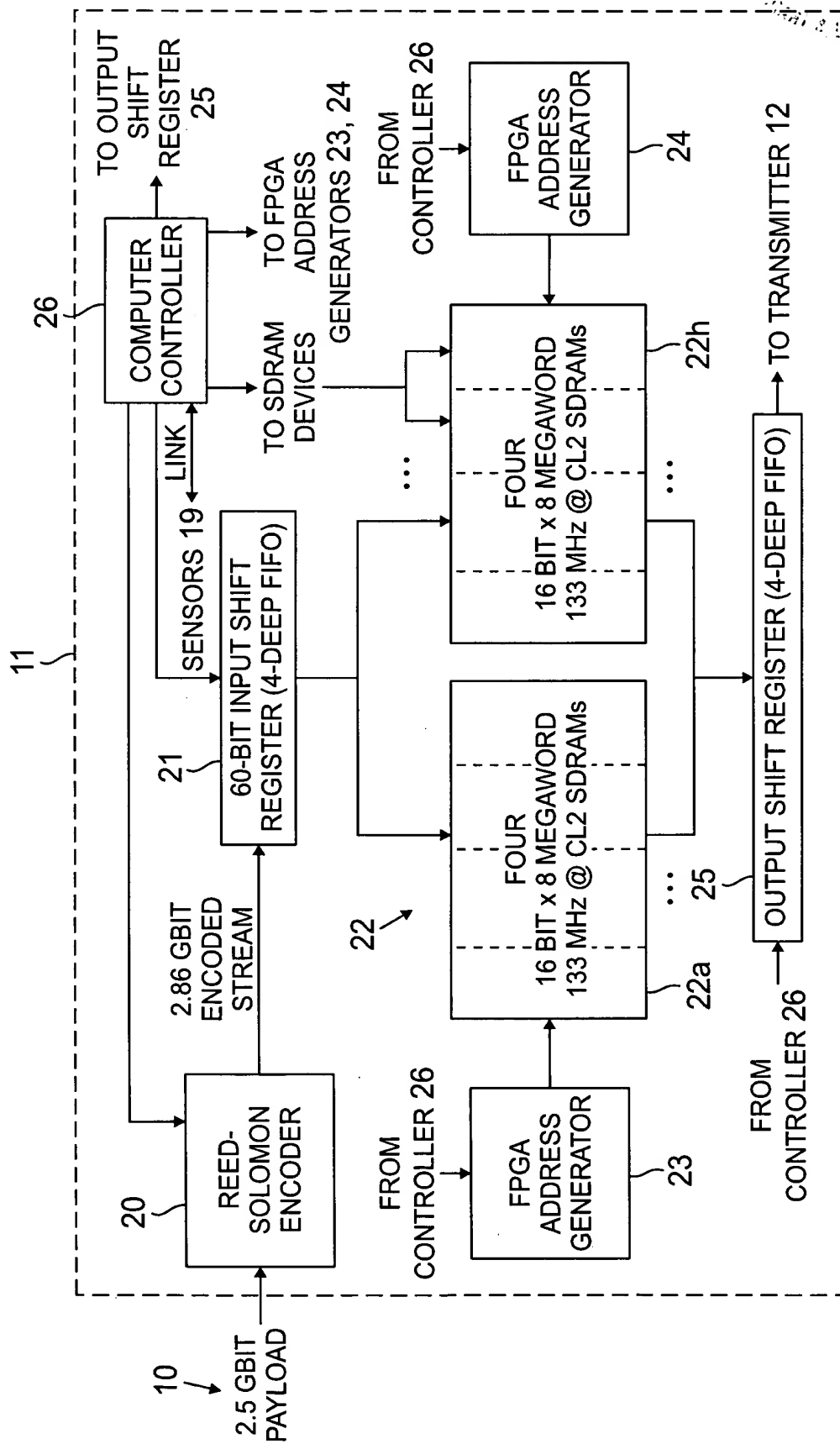
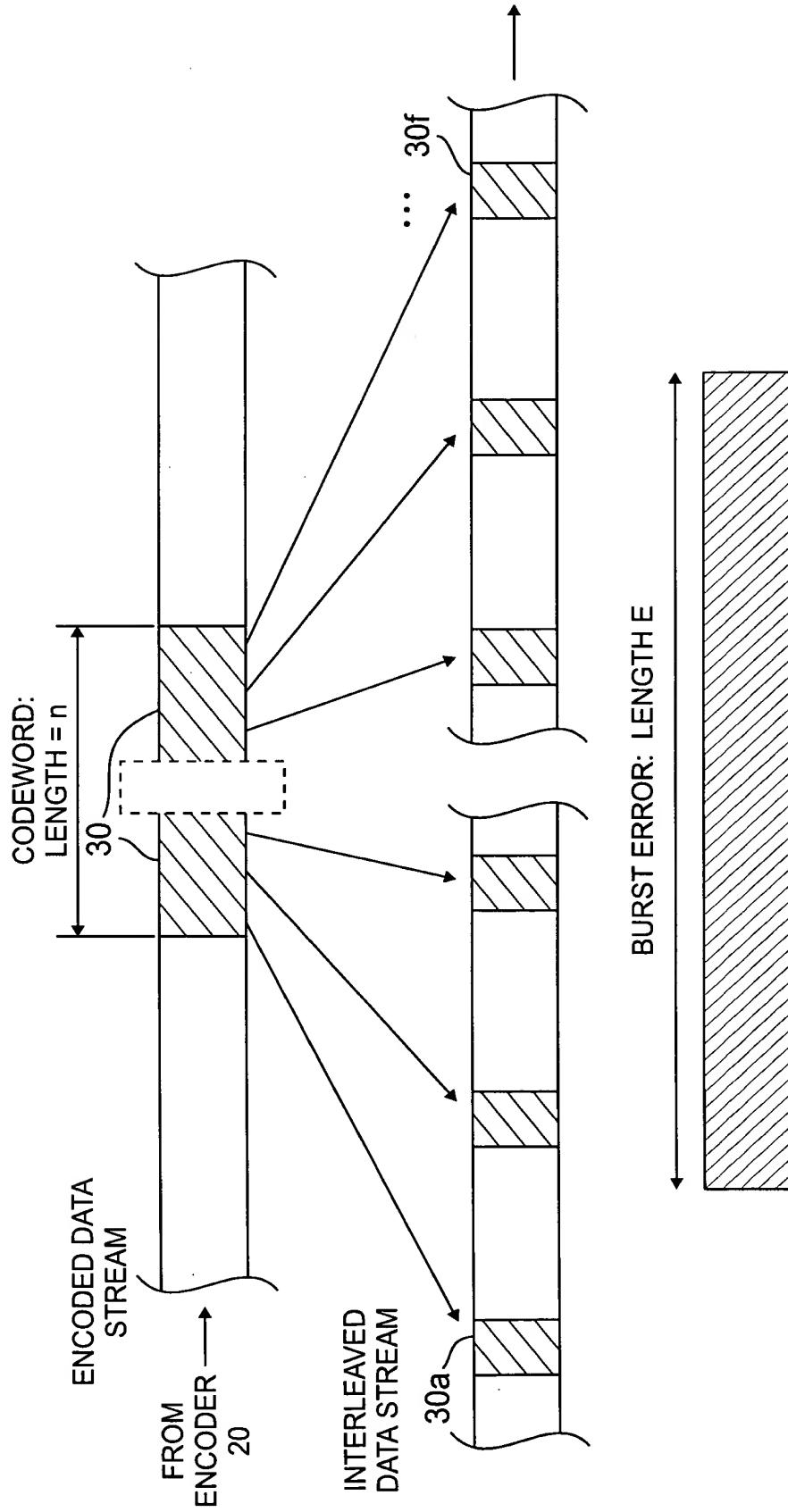


FIG. 2



INTERLEAVING THE ENCODED PAYLOAD

FIG. 3



FROM REED-SOLOMON ENCODER 20

DENOTE THE 60 BIT SEGMENTS OF  
THE CODEWORDS BY A 2-TUPLE:

(n, m): n = CODEWORD NUMBER; m = 60-BIT  
SEGMENT WITHIN THE CODEWORD  
INDICATED

n = 1, 2, ..., 156250

m = 1, 2, ..., 34 FOR 2040 CODEWORD  
LENGTH

INTERLEAVE THE FIRST BLOCK  
OF 156,250 CODEWORDS

THEN, AFTER INTERLEAVING THE FIRST BLOCK  
OF 156,250 CODEWORDS THE SEQUENCE IS:

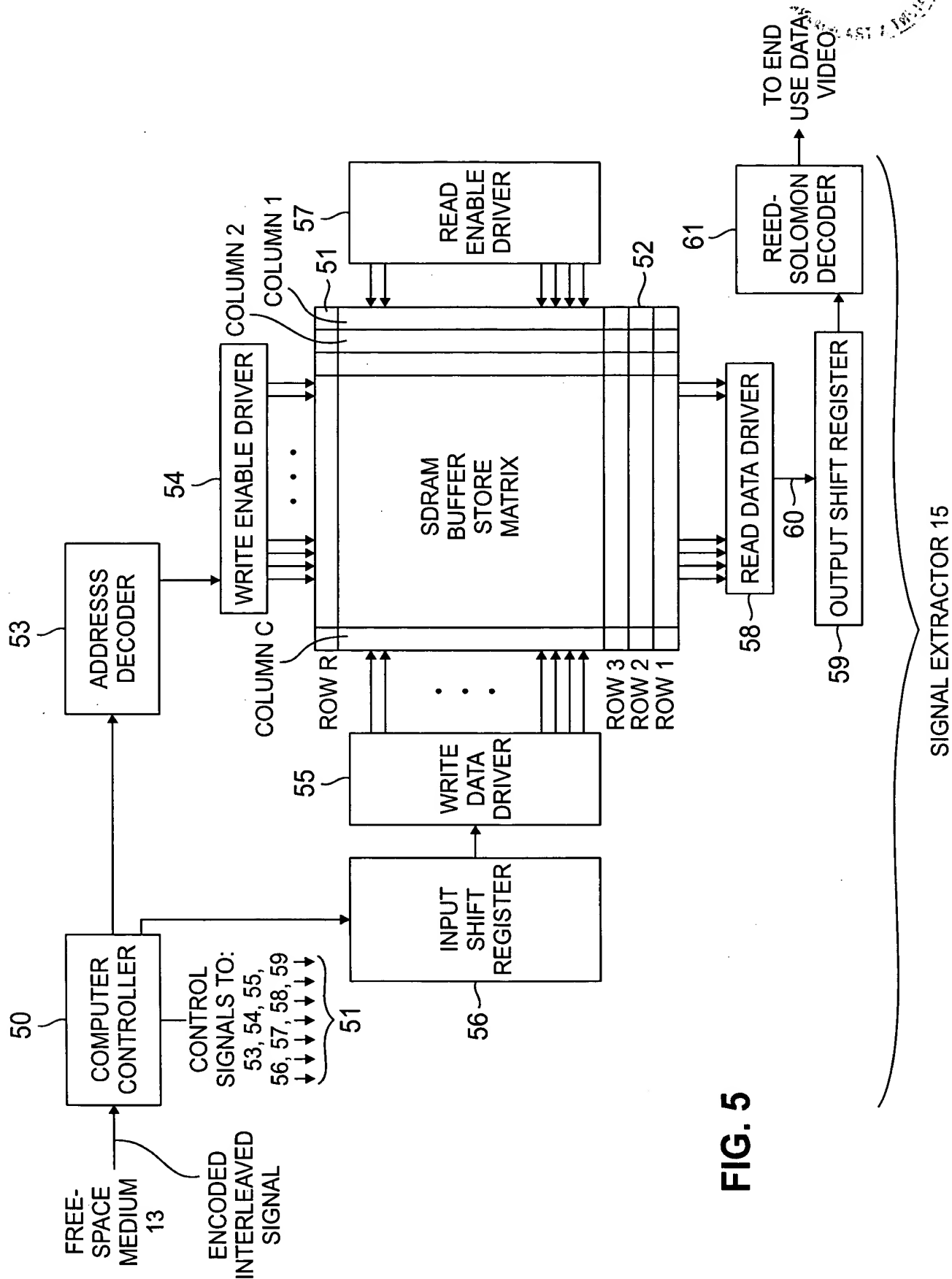
[1,1 2,1 3,1 ..... 156250,1] [1,2 2,2 ..... 156250,2] ..... [1,34 2,34 ... 156250,34]

INTERLEAVE THE SECOND  
BLOCK OF 156,250 CODEWORDS

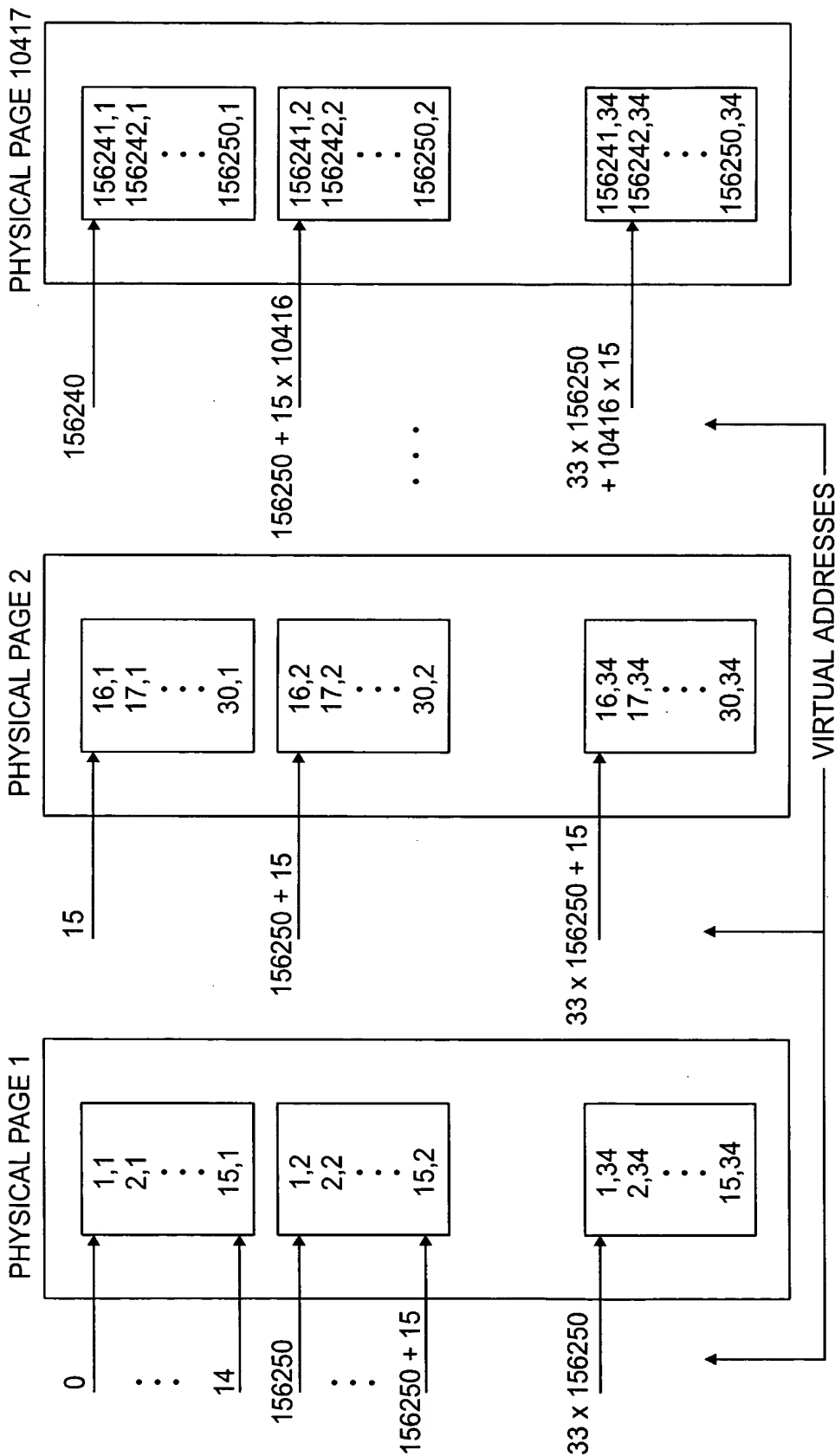
⋮

COMPLETE  
INTERLEAVING

FIG. 4



(VIRTUAL PAGES ARE 15 WORDS IN LENGTH)



REMAPPING ADDRESSES TO BALANCE  
"READ" VS. "WRITE" OVERHEAD IN TERMS  
OF SDRAM PAGE CHANGES

FIG. 6A

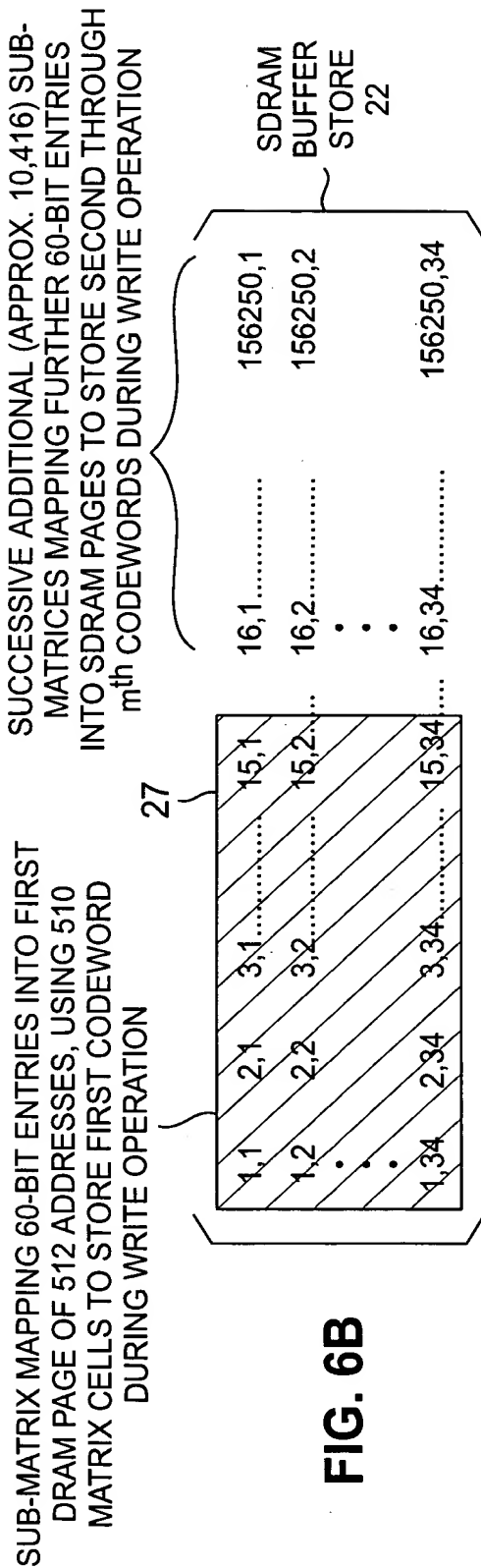
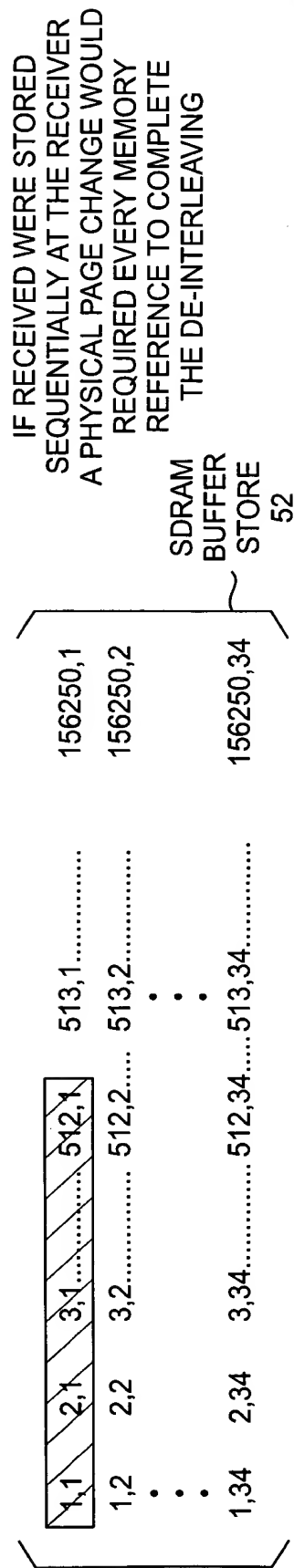


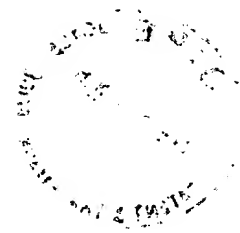
FIG. 6B

CODEWORD SEGMENTS EXPRESSED AS A MATRIX



CROSS-HATCHED SUBMATRIX INDICATES SEGMENT OF  
RECEIVED MATRIX THAT WOULD BE HELD ON ONE  
512 ADDRESS PAGE IF RECEIVER STORED  
ENTRIES SEQUENTIALLY

FIG. 6C



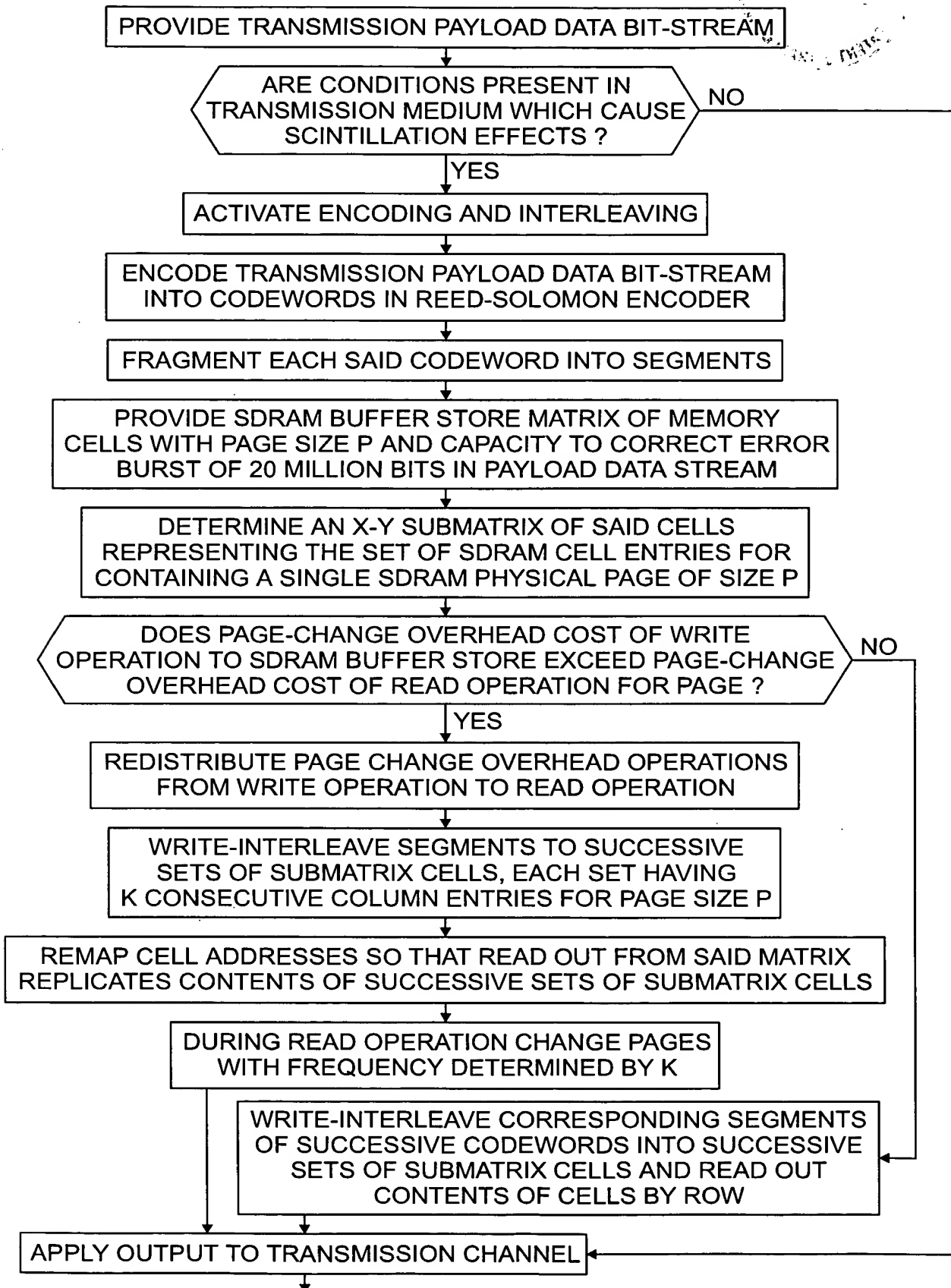
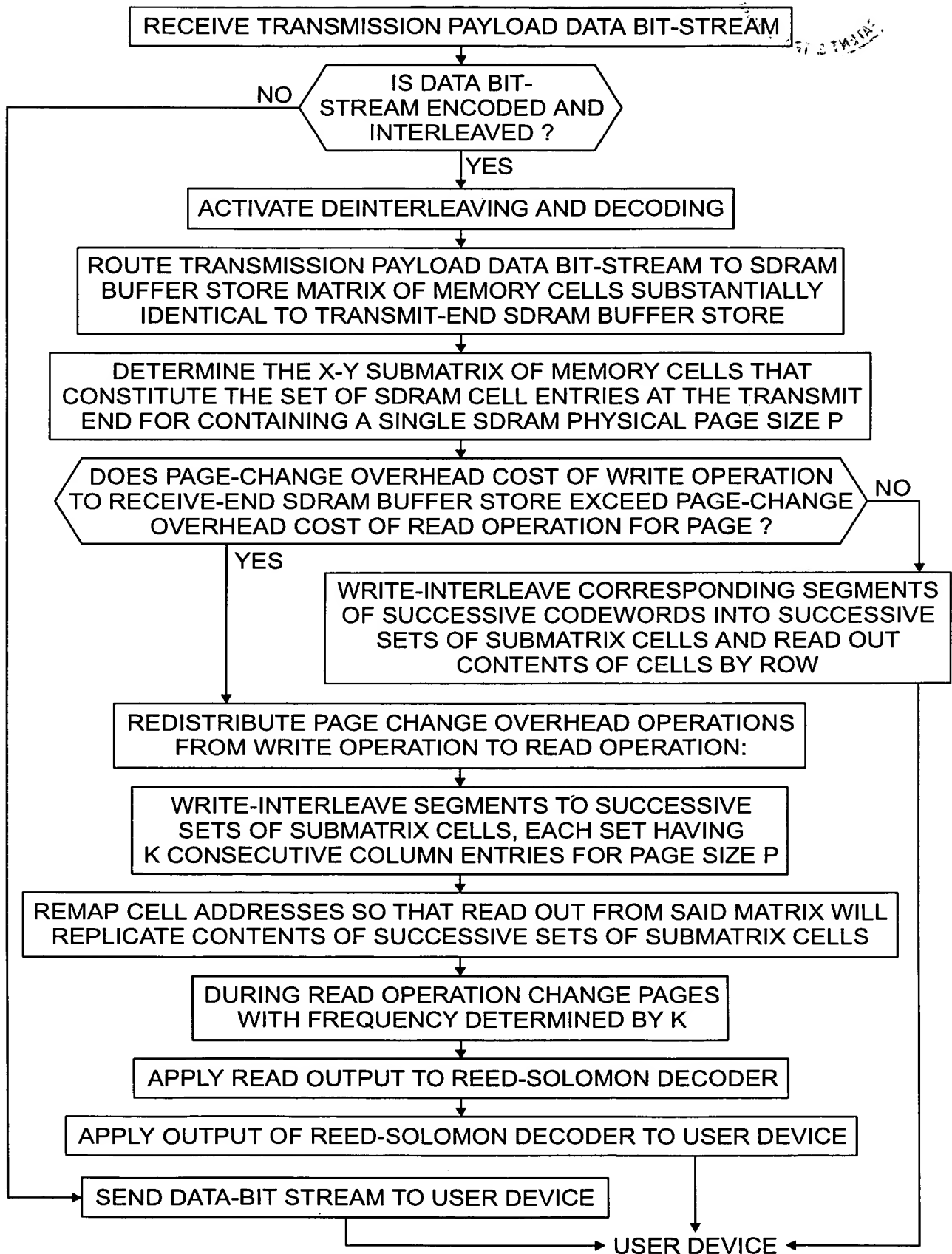


FIG. 7A

FLOW CHART OF PROCESS  
AT TRANSMITTER END



FLOW CHART OF PROCESS  
AT RECEIVER END

FIG. 7B